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7590

05/19/2004

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Mitchell S. Bigel, #29,614	(Depositor's name)
<i>[Signature]</i>	(Signature)
July 19, 2004	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/689,435	10/20/2003	Soo-Hyoung Lee	5649-1176	2809

TITLE OF INVENTION: SYNCHRONIZING CIRCUITS AND METHODS FOR PARALLEL PATH ANALOG-TO-DIGITAL CONVERTERS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	08/19/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
MAI, LAM T	2819	341-155000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1 **Myers Bigel Sibley & Sajovec**

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Samsung Electronics Co., Ltd.

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Republic of KoreaPlease check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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